



# APPROVAL SHEET

Approval Specification	Customer's Approval Certificate
<p><b>TO:</b></p> <p><b>Part No.:</b></p> <p><b>Customer's Part No.:</b></p>	<p>Please return this copy as a certification of your approval</p> <p><b>Checked &amp; Approved by:</b></p> <p><b>Date:</b></p>

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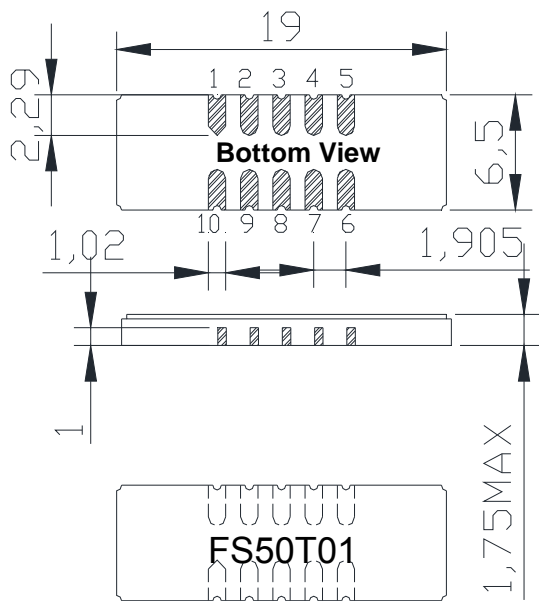
**Application**

- Low-loss SAW component
- Low amplitude ripple
- Sharp rejections at both out-bands
- Usable passband 20KHz

**Features**

- Ceramic Package for **Surface Mounted Technology (SMT)**
- **RoHS** compatible
- Package size 19.50x6.50x1.75mm<sup>3</sup>
- Package Code SMD19
  
- **Electrostatic Sensitive Device(ESD)**

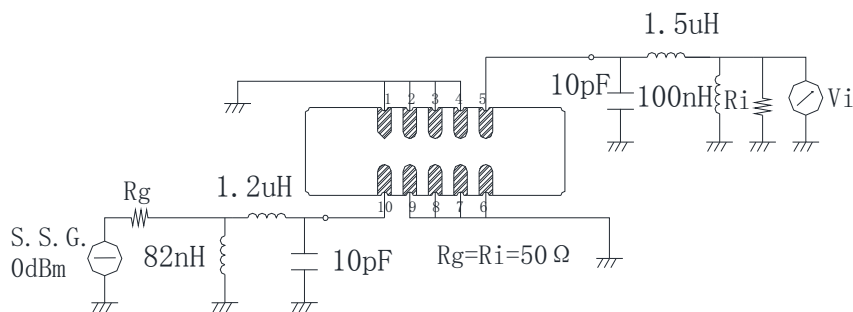
**Package Dimensions (Unit: mm)**



**Pin Configuration**

Pin No.	Description
10	Input
5	Output
1,2,3,4,6,7,8,9	Ground

**Test Circuit (Bottom View)**



**Performance****Maximum Rating**

Item		Value	Unit
DC Voltage	V <sub>DC</sub>	3	V
Operation Temperature	T	-40 ~ +85	°C
Storage Temperature	T <sub>stg</sub>	-55 ~ +125	°C
RF Power Dissipation	P	10	dBm

**Electronic Characteristics**

Test Temperature: 25°C ± 2°C

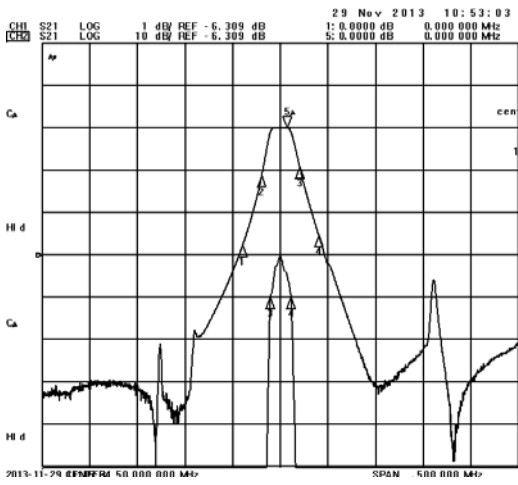
Terminating source impedance: 50Ω

Terminating load impedance: 50Ω

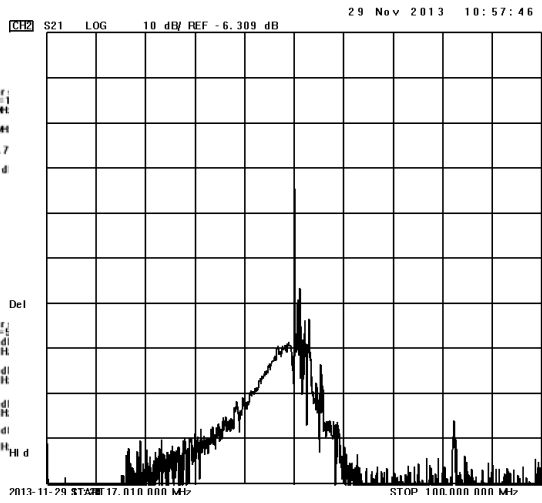
Item		Minimum	Typical	Maximum	Unit
Center Frequency	f <sub>c</sub>	49.98	50.00	50.02	MHz
Insertion Loss(min)	IL		6.0	6.5	dB
Amplitude Ripple (p-p)	Δα		0.5	1.0	dB
1 dB Bandwidth	BW <sub>1dB</sub>	20.0	22.0		KHz
Absolute Attenuation	α				
	49.50 MHz	50.0	60.0		dB
	49.96 MHz	12.0	25.0		dB
	49.98 MHz	3.0	10.0		dB
	50.02MHz	3.0	10.0		dB
	50.04MHz	12.0	25.0		dB
	50.50 MHz	50.0	55.0		dB
Input VSWR			2.5	3.0:1	/
Output VSWR			2.5	3.0:1	/

Frequency Characteristics

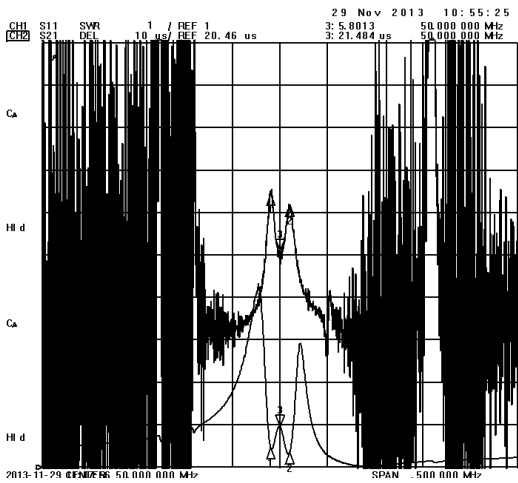
Frequency Response



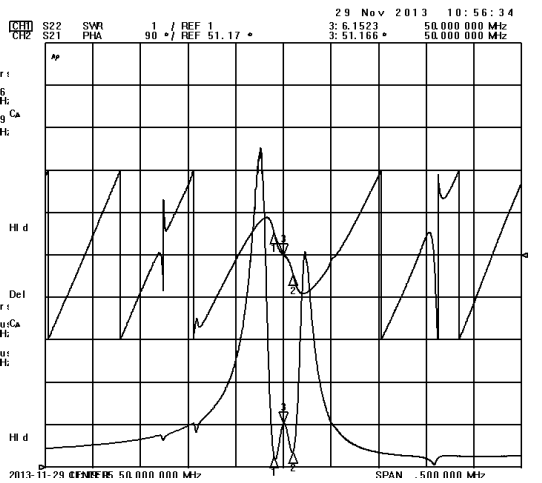
Frequency Response (wideband)



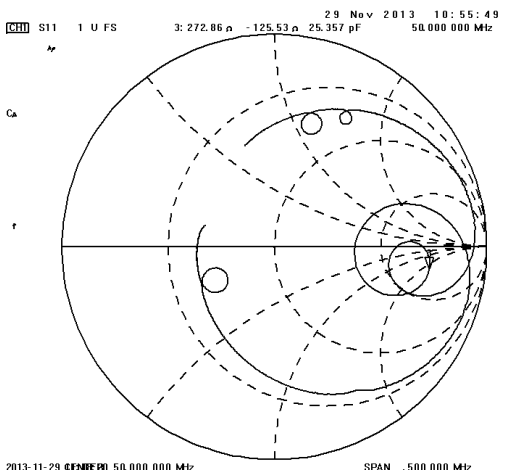
Delay Ripple & S11 VSWR



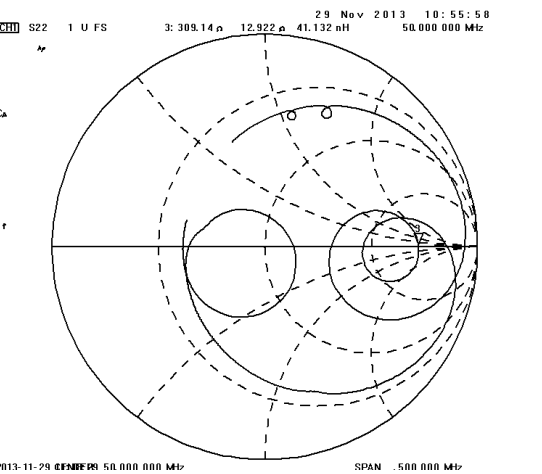
Phase Linearity & S22 VSWR



S11 Smith Chart



S22 Smith Chart





**Notes**

1. As a result of the particularity of inner structure of SAW products, it easy to be breakdown by electrostatic, so we should pay attention to **ESD protect** in the test.
2. **Static voltage** between signal load and ground may cause deterioration and destruction of the component. Please avoid static voltage.
3. **Ultrasonic cleaning** may cause deterioration and destruction of the component. Please avoid ultrasonic cleaning.
4. Only leads of component may **be soldered**. Please avoid soldering another part of component.
5. There is a close relationship between the device's performance and **matching network**. The specifications of this device are based on the test circuit shown above. L and C values may change depending on board layout. Values shown are intended as a guide only.