



APPROVAL SHEET

Approval Specification	Customer's Approval Certificate
<p>TO:</p> <p>Part No.:</p> <p>Customer's Part No.:</p>	<p>Please return this copy as a certification of your approval</p> <p>Checked & Approved by:</p> <p>Date:</p>

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Part No.	:	SF0146
Pages	:	6
Date	:	2013/3/25
Revision	:	1.0

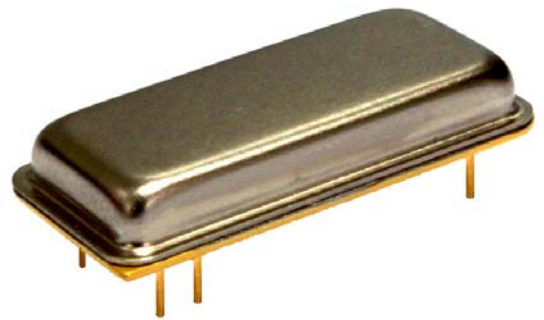
Prepared by:	
Checked by:	
Approved by:	

Application

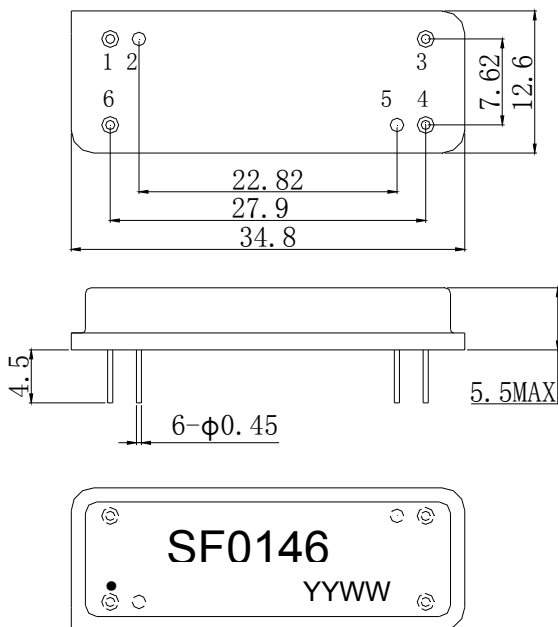
- Low amplitude ripple
- Sharp rejections at both out-bands
- Usable passband 200 KHz

Features

- RoHS compatible
- Package size 34.8x12.6x5.50mm³
- Package Code DIP3512J
- Electrostatic Sensitive Device(ESD)



Package Dimensions (Unit: mm)



Pin Configuration

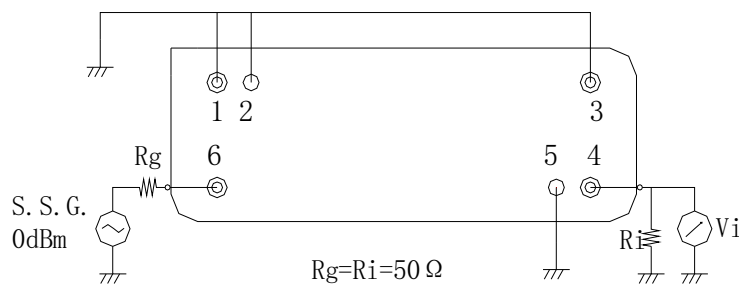
Pin No.	Description
6	Input
4	Output
1,2,3,5	Ground

Marking Description

S	Trademark
F	SAW Filter
0146	Part Number
●	Pin 1
YYWW	Year Code & Week Code

*Fig: If the products produced in 06th week of 2012, The year code & week code is 1206.

Test Circuit



Performance**Maximum Rating**

Item		Value	Unit
DC Voltage	V_{DC}	3	V
Operation Temperature	T	-40 ~ +85	°C
Storage Temperature	T_{stg}	-55 ~ +125	°C
RF Power Dissipation	P	10	dBm

Electronic Characteristics

Test Temperature: $25^{\circ}\text{C} \pm 2^{\circ}\text{C}$

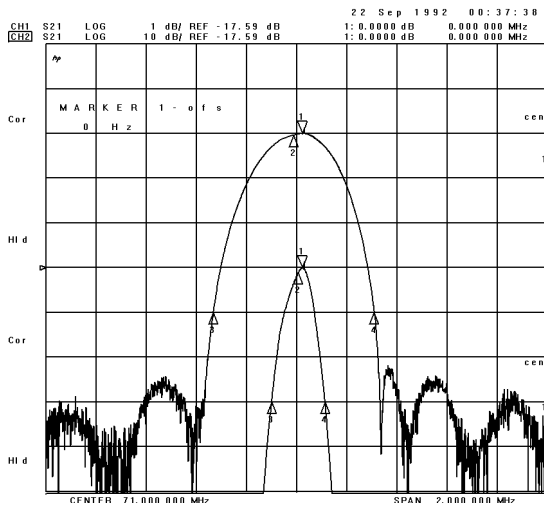
Terminating source impedance: 50Ω

Terminating load impedance: 50Ω

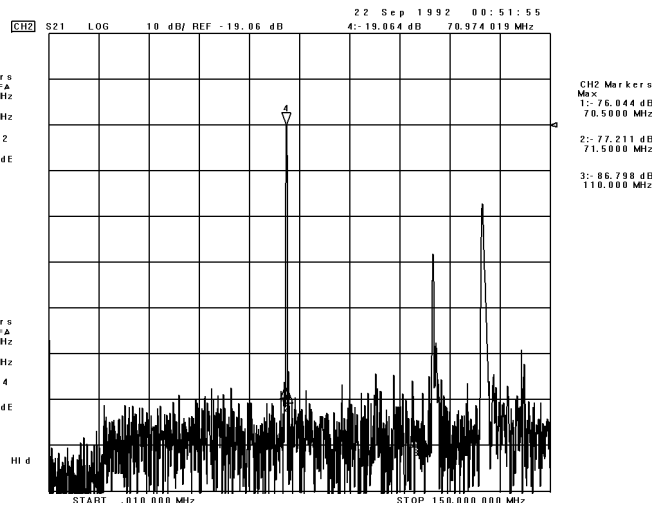
Item		Minimum	Typical	Maximum	Unit
Center Frequency	f_c		71.0		MHz
Insertion Loss(min)	IL		17.6	18.0	dB
Amplitude Ripple (p-p)	Δa		0.3	1.0	dB
3 dB Bandwidth	BW_{3dB}	200	210		KHz
Absolute Attenuation	a				
	DC-70.40 MHz	50.0	57.0		dB
	70.40 MHz	55.0	58.0		dB
	70.60MHz	55.0	68.0		dB
	70.80 MHz	9.0	10.0		dB
	71.20 MHz	9.0	13.0		dB
	71.40 MHz	55.0	55.0		dB
	71.60 MHz	55.0	60.0		dB
	71.60-110.00 MHz	50.0	53.0		dB

Frequency Characteristics

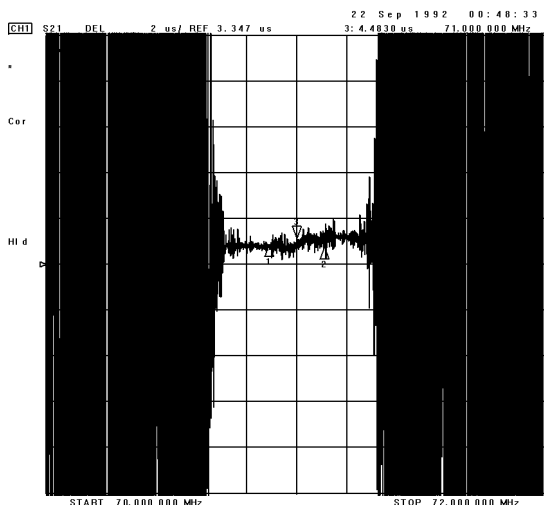
Frequency Response



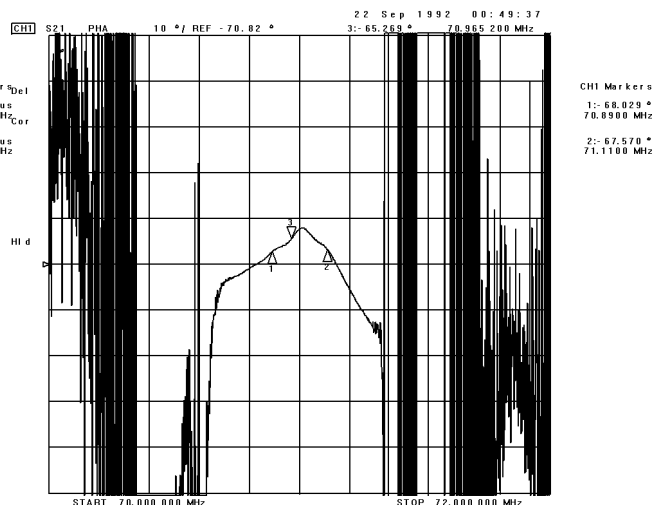
Frequency Response (wideband)



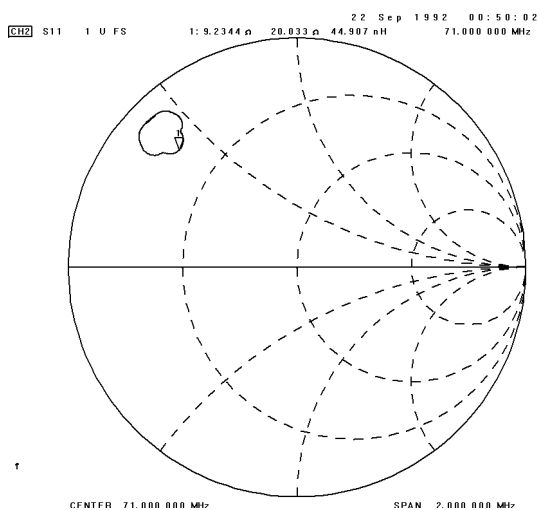
Delay Ripple & S11 VSWR



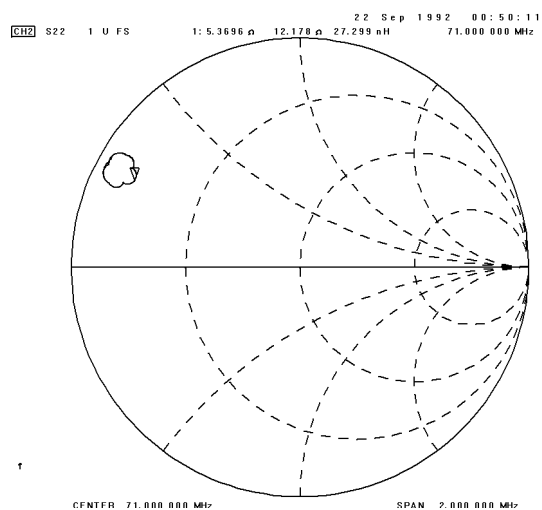
Phase Linearity & S22 VSWR



S11 Smith Chart



S22 Smith Chart



Notes

1. As a result of the particularity of inner structure of SAW products, it easy to be breakdown by electrostatic, so we should pay attention to **ESD protect** in the test.
2. **Static voltage** between signal load and ground may cause deterioration and destruction of the component. Please avoid static voltage.
3. **Ultrasonic cleaning** may cause deterioration and destruction of the component. Please avoid ultrasonic cleaning.
4. Only leads of component may **be soldered**. Please avoid soldering another part of component.
5. There is a close relationship between the device's performance and **matching network**. The specifications of this device are based on the test circuit shown above. L and C values may change depending on board layout. Values shown are intended as a guide only.