



APPROVAL SHEET

Approval Specification	Customer's Approval Certificate
<p>TO:</p> <p>Part No.:</p> <p>Customer's Part No.:</p>	<p>Please return this copy as a certification of your approval</p> <p>Checked & Approved by:</p> <p>Date:</p>

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Part No.	:	SF1193
Pages	:	6
Date	:	2013/3/21
Revision	:	1.1

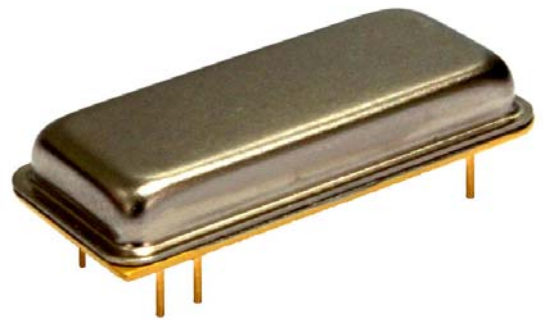
Prepared by:	郑宝琴
Checked by:	
Approved by:	

Application

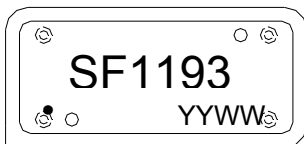
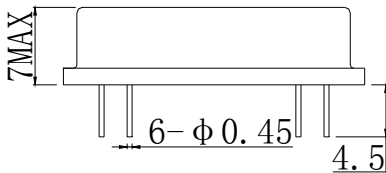
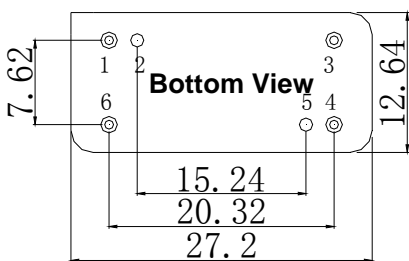
- Low amplitude ripple
- Sharp rejections at both out-bands
- Usable passband 4 MHz

Features

- RoHS compatible
- Package size 14.5x10.0x5.10mm³
- Package Code DIP2712
- Electrostatic Sensitive Device(ESD)



Package Dimensions (Unit: mm)



Pin Configuration

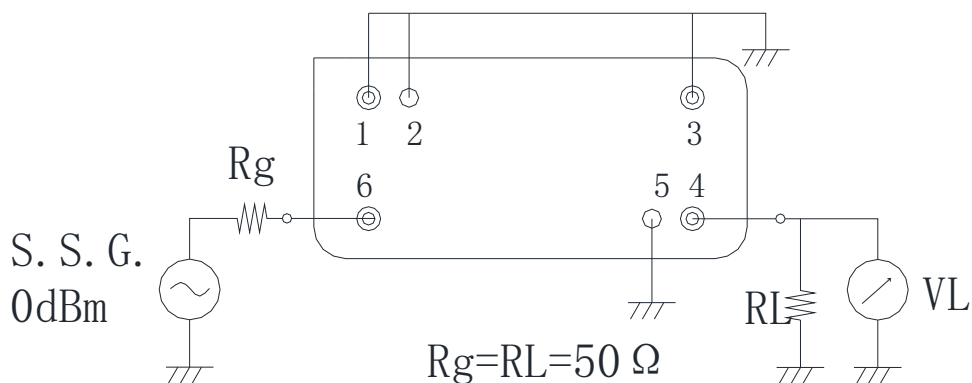
Pin No.	Description
6	Input
4	Output
1,2,3,5	Ground

Marking Description

S	Trademark
F	SAW Filter
1193	Part Number
●	Pin 1
YYWW	Year Code & Week Code

*Fig: If the products produced in 06th week of 2012, The year code & week code is 1206.

Test Circuit(Bottom View)



Performance**Maximum Rating**

Item		Value	Unit
DC Voltage	V_{DC}	3	V
Operation Temperature	T	-40 ~ +85	°C
Storage Temperature	T_{stg}	-55 ~ +125	°C
RF Power Dissipation	P	10	dBm

Electronic Characteristics

Test Temperature: $25^{\circ}\text{C} \pm 2^{\circ}\text{C}$

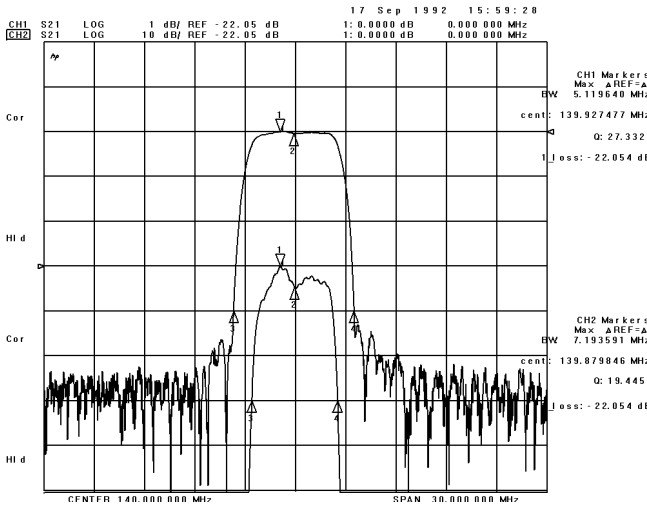
Terminating source impedance: 50Ω

Terminating load impedance: 50Ω

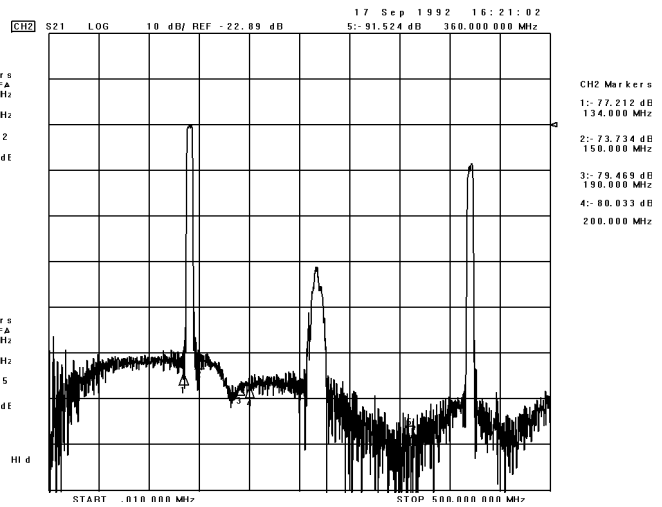
Item		Minimum	Typical	Maximum	Unit
Center Frequency	f_c	139.8	140.0	140.2	MHz
Insertion Loss(min)	IL		22.1	25.0	dB
Amplitude Ripple (p-p) 138.00-142.00MHz	$\Delta\alpha$		0.8	1.0	dB
1 dB Bandwidth	BW_{1dB}	4.3	4.5		MHz
40 dB Bandwidth	BW_{40dB}		7.1	7.5	MHz
Absolute Attenuation	α				
	DC -134.00 MHz	45.0	50.0		dB
	150.00-250.00MHz	45.0	48.0		dB

Frequency Characteristics

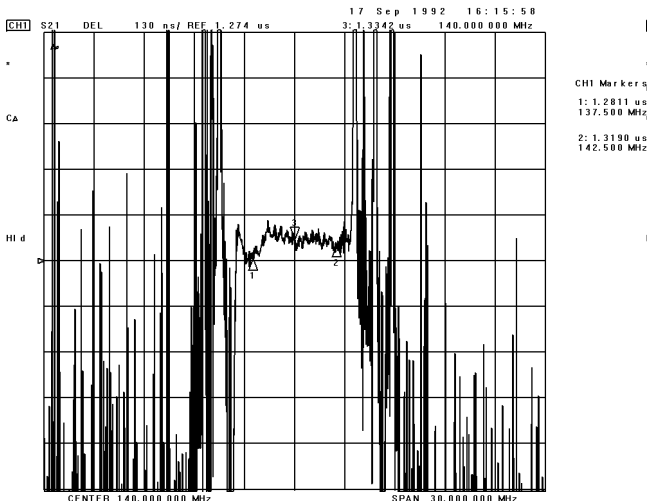
Frequency Response



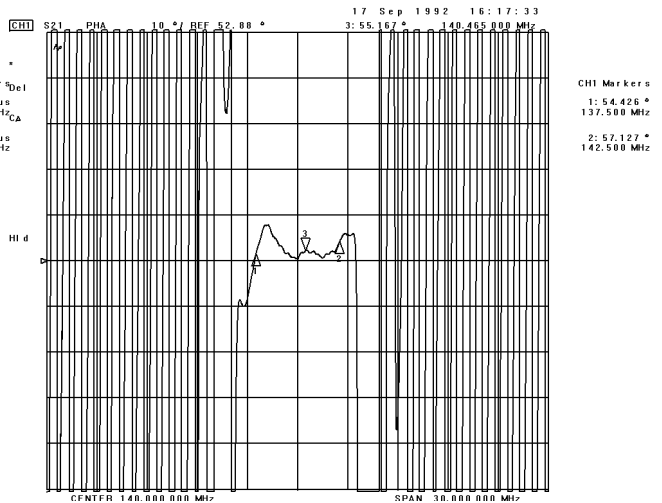
Frequency Response (wideband)



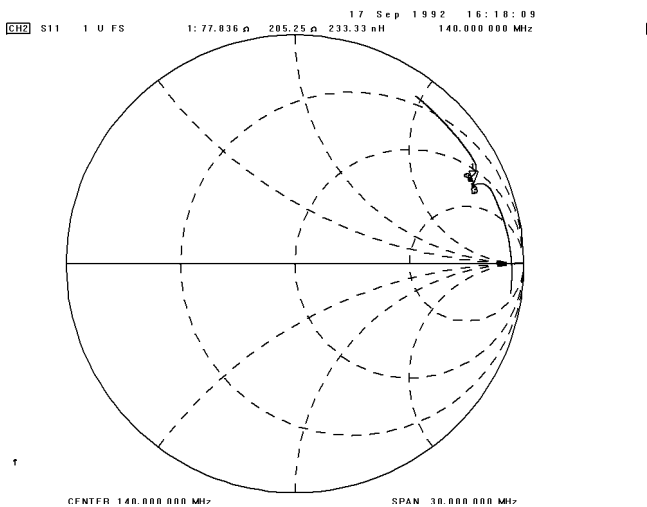
Delay Ripple



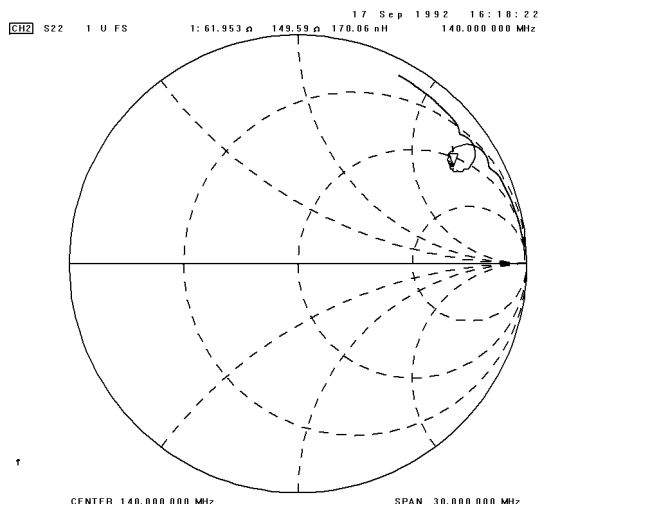
Phase Linearity



S11 Smith Chart



S22 Smith Chart



Notes

1. As a result of the particularity of inner structure of SAW products, it is easy to be broken down by electrostatic, so we should pay attention to **ESD protect** in the test.
2. **Static voltage** between signal load and ground may cause deterioration and destruction of the component. Please avoid static voltage.
3. **Ultrasonic cleaning** may cause deterioration and destruction of the component. Please avoid ultrasonic cleaning.
4. Only leads of component may **be soldered**. Please avoid soldering another part of component.
5. There is a close relationship between the device's performance and **matching network**. The specifications of this device are based on the test circuit shown above. L and C values may change depending on board layout. Values shown are intended as a guide only.