



APPROVAL SHEET

Approval Specification	Customer's Approval Certificate
TO: Part No.: Customer's Part No.:	Please return this copy as a certification of your approval Checked & Approved by: Date:

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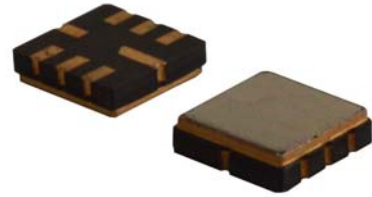


Part No.	:	SF2492
Pages	:	6
Date	:	2015/9/29
Revision	:	1.0

Prepared by:	梁浩
Checked by:	
Approved by:	

Application

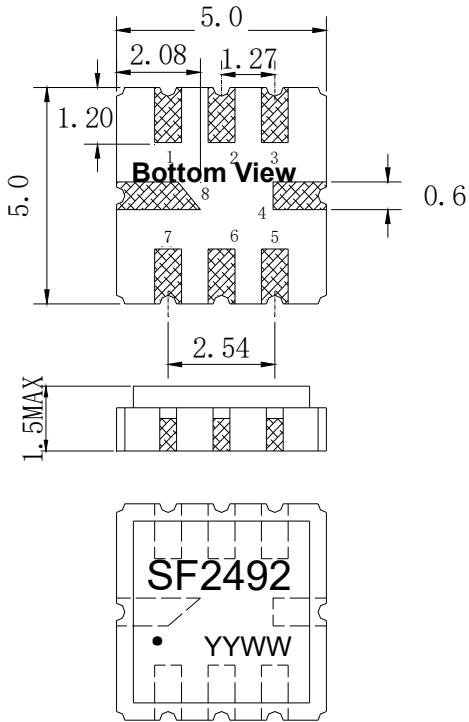
- Low -loss SAW component
- Low amplitude ripple
- Sharp rejections at both out-bands
- Passband 12 MHz



Features

- Ceramic Package for **Surface Mounted Technology (SMT)**
- **RoHS** compatible
- Package size 5.00x5.00x1.50mm³
- Package Code QCC8C
- **Electrostatic Sensitive Device(ESD)**

Package Dimensions (Unit: mm)



Pin Configuration

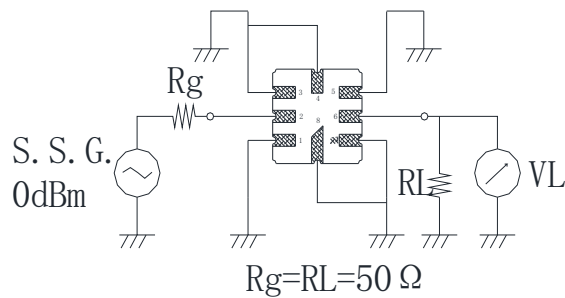
Pin No.	Description
2	Input
6	Output
1,3,4,5,7,8	Ground

Marking Description

S	Trademark
F	SAW Filter
2492	Part Number
●	Pin 1
YYWW	Year Code & Week Code

*Fig: If the products produced in 06th week of 2012, The year code & week code is 1206.

Test Circuit(Bottom View)



Performance**Maximum Rating**

Item		Value	Unit
DC Voltage	V_{DC}	3	V
Operation Temperature	T	-40 ~ +55	°C
Storage Temperature	T_{stg}	-55 ~ +125	°C
RF Power Dissipation	P	10	dBm

Electronic Characteristics

Test Temperature: $25^{\circ}\text{C} \pm 2^{\circ}\text{C}$

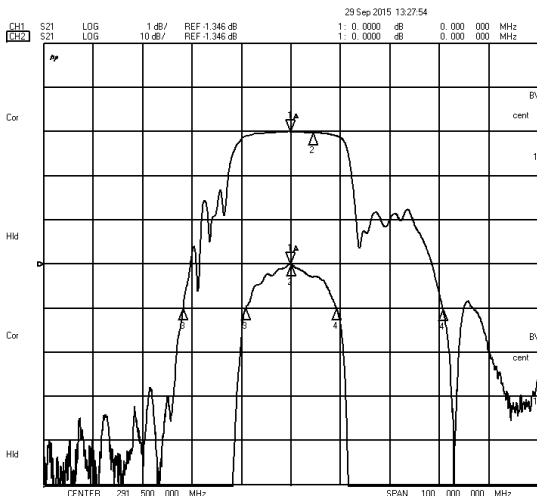
Terminating source impedance: 50Ω

Terminating load impedance: 50Ω

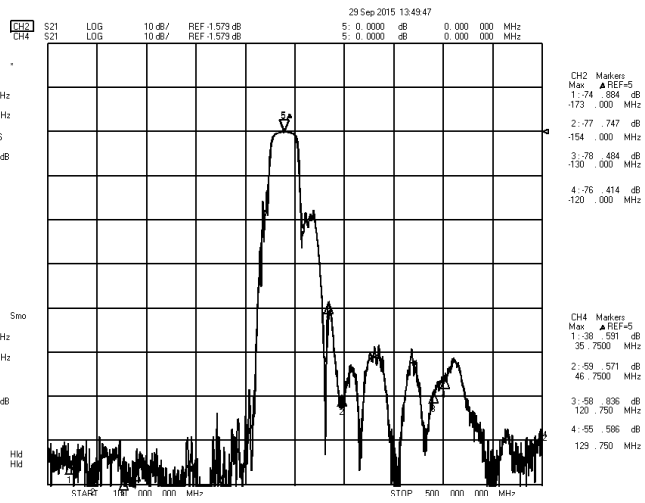
Item		Minimum	Typical	Maximum	Unit
Center Frequency	f_c		291.5		MHz
Insertion Loss(min)	IL		1.34	2.0	dB
Amplitude Ripple 284.50-298.50 MHz	$\Delta\alpha$		0.5	1.0	dB
1 dB Useable Bandwidth			12.0		
1 dB Bandwidth	BW_{1dB}	14.0	18.3		MHz
Shape Factor	BW_{40dB} / BW_{3dB}		2.6	3.5	/
Absolute Attenuation	α				
118.00-137.00 MHz	dB	50.0	65.0		dB
161.00-171.00 MHz	dB	50.0	67.0		dB
327.00-338.00 MHz	dB	30.0	38.0		dB
412.00-421.00 MHz	dB	40.0	53.0		dB
Input VSWR 284.50-298.50 MHz			1.5:1	2.0:1	/
Output VSWR 284.50-298.50 MHz			1.5:1	2.0:1	/

Frequency Characteristics

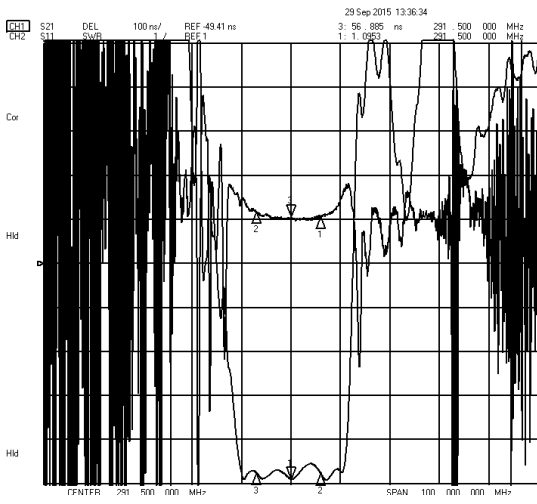
Frequency Response



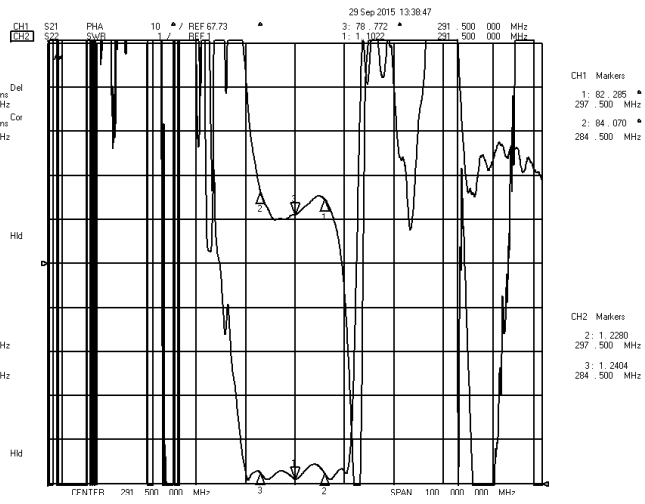
Frequency Response (wideband)



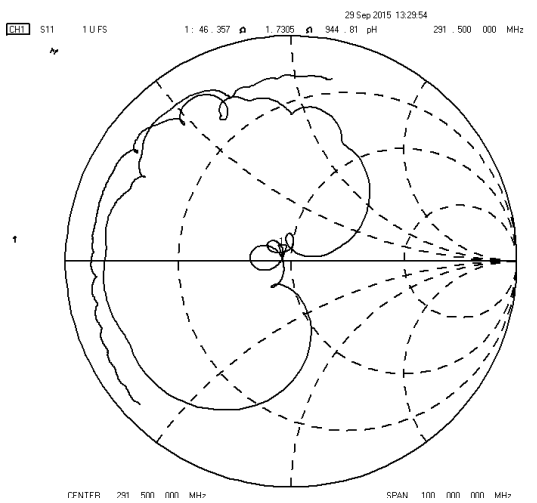
Delay Ripple & S11 VSWR



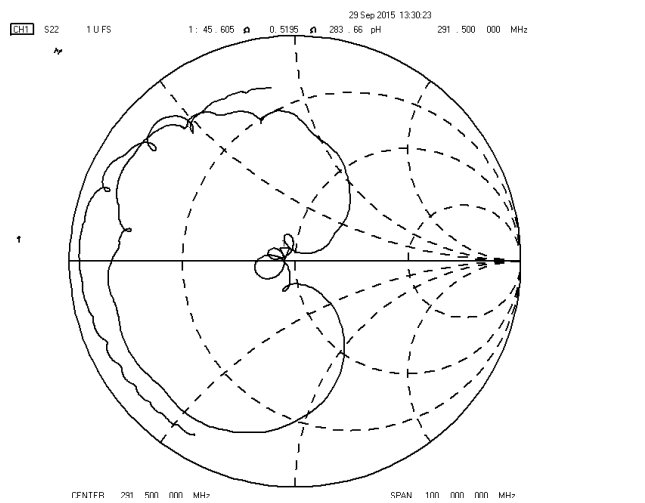
Phase Linearity & S22 VSWR



S11 Smith Chart



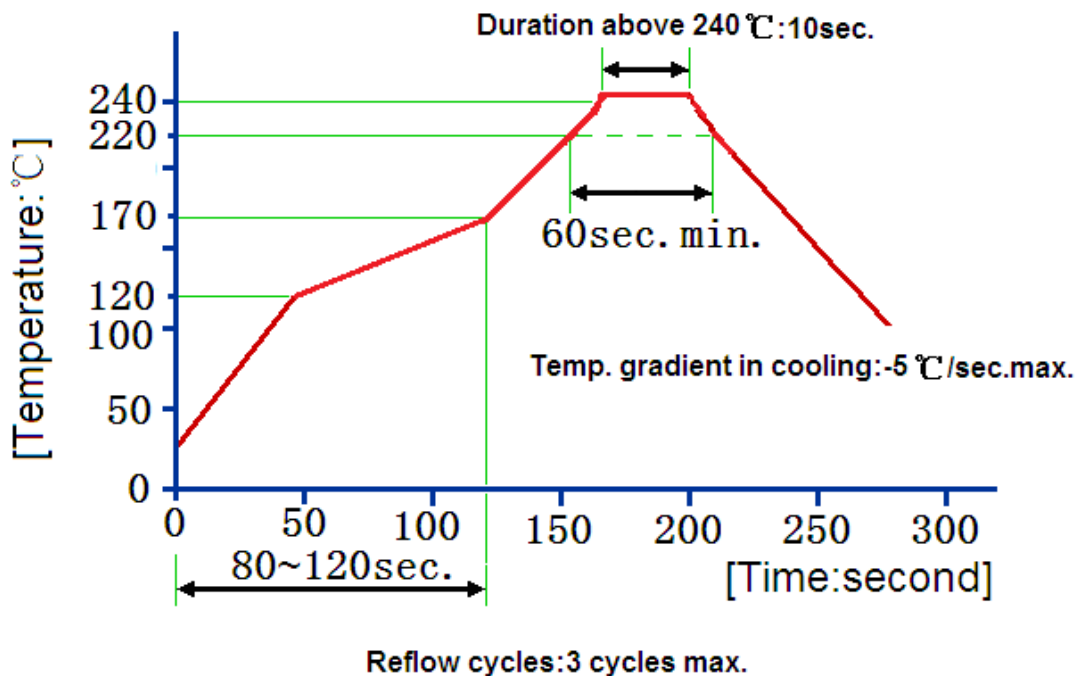
S22 Smith Chart



Reliability (The SAW components shall remain electrical performance after tests)

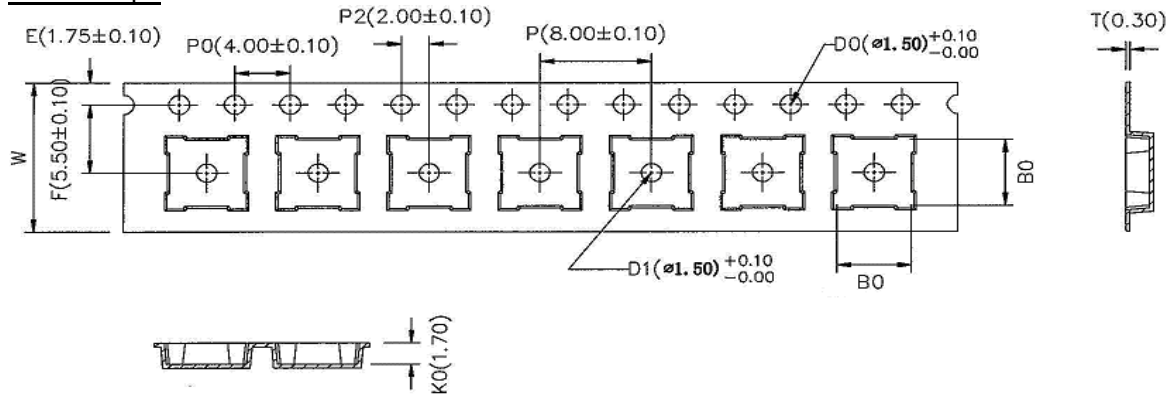
No.	Test item	Test condition
1	Temperature Storage	(1) Temperature: 85°C±2°C , Duration: 250h , Recovery time: 2h±0.5h (2) Temperature: -55°C±3°C , Duration: 250h ,Recovery time: 2h±0.5h
2	Humidity Test	Conditions: 60°C±2°C , 90~95% RH Duration: 250h
3	Thermal Shock	Heat cycle conditions: TA=-55°C±3°C, TB=85°C±2°C, t1=t2=30min, Switch time: ≤3min, Cycle time: 100 times, Recovery time: 2h±0.5h.
4	Vibration Fatigue	Frequency of vibration: 10~55Hz Amplitude:1.5mm Directions: X,Y and Z Duration: 2h
5	Drop Test	Cycle time: 10 times Height: 1.0m
6	Solder Ability Test	Temperature: 245°C±5°C Duration: 3.0s--5.0s Depth: DIP--2/3 , SMD--1/5
7	Resistance to Soldering Heat	(1)Thickness of PCB:1mm , Solder condition: 260°C±5°C , Duration: 10±1s (2)Temperature of Soldering Iron: 350°C±10°C , Duration: 3~4s , Recovery time : 2 ± 0.5h

Recommended Reflow Soldering Diagram



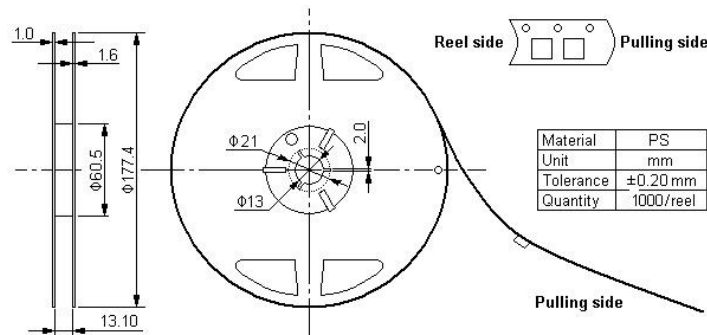
Packing Information

Carrier Tape



* B0: 5.35 for QCC8C; 4.15 for DCC6/QCC8B; 3.35 for DCC6C/QCC8D

Reel Dimensions



Outer Packing

Type	Quantity	Dimension	Description	Weight
Internal box	1000	190×188×42	carton box 2 reel / internal box 5 boxes / external box	0.18
External box	10000	235×205×210		1.80

Unit: mm

Unit: kg

Notes

1. As a result of the particularity of inner structure of SAW products, it is easy to be breakdown by electrostatic, so we should pay attention to **ESD protect** in the test.
2. **Static voltage** between signal load and ground may cause deterioration and destruction of the component. Please avoid static voltage.
3. **Ultrasonic cleaning** may cause deterioration and destruction of the component. Please avoid ultrasonic cleaning.
4. Only leads of component may be **soldered**. Please avoid soldering another part of component.
5. There is a close relationship between the device's performance and **matching network**. The specifications of this device are based on the test circuit shown above. L and C values may change depending on board layout. Values shown are intended as a guide only.