



APPROVAL SHEET

Approval Specification	Customer's Approval Certificate
TO: Part No.: Customer's Part No.:	Please return this copy as a certification of your approval Checked & Approved by: Date:

BEIJING ZHONGXUN SIFANG SCIENCE & TECHNOLOGY CO.,LTD.

Tel: +86-010-58937383
 Fax: +86-010-58937263
 E-mail: bjzxsf@bjzxsf.net
 Website: <http://www.bjzxsf.net>
 Add: No 201, Block A. Building 3. Yongjie Beilu
 Yongfeng high-tech industrial base
 Haidian District Beijing city



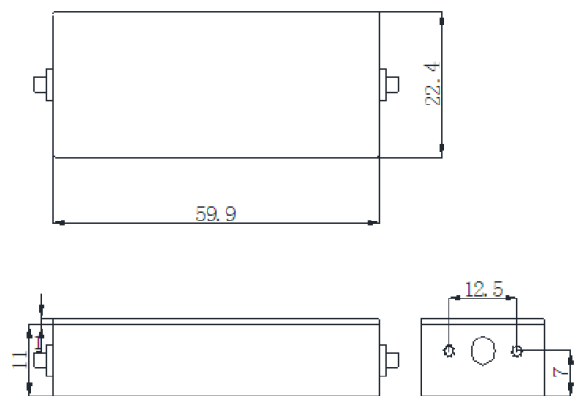
Part No.	:	SDL3101
Pages	:	6
Date	:	2016/3/14
Revision	:	1.0

Prepared by:	梁浩
Checked by:	
Approved by:	

Features

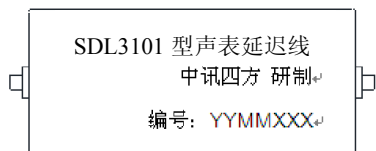
- RoHS compatible
- Package size 59.9x22.4x11.0mm³
- Package Code MXSS6023-P2BA
- Electrostatic Sensitive Device(ESD)

Package Dimensions (Unit: mm)



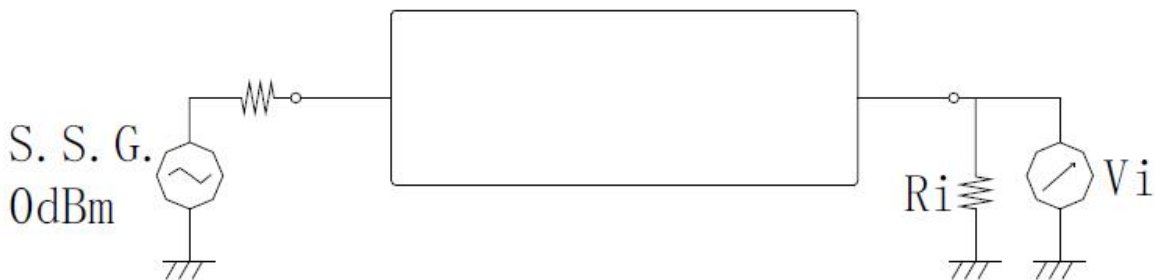
Marking Description

SDL	Saw Delay-Line
3101	Part Number
YYMM	Year Code & Month Code
XXX	Serial Number



Connected By SMA-K

Test Circuit



Performance**Maximum Rating**

Item		Value	Unit
DC Voltage	V_{DC}	3	V
Operation Temperature	T	-40 ~ +85	$^{\circ}C$
Storage Temperature	T_{stg}	-55 ~ +125	$^{\circ}C$
RF Power Dissipation	P	10	dBm

Electronic Characteristics

Test Temperature: $25^{\circ}C \pm 2^{\circ}C$

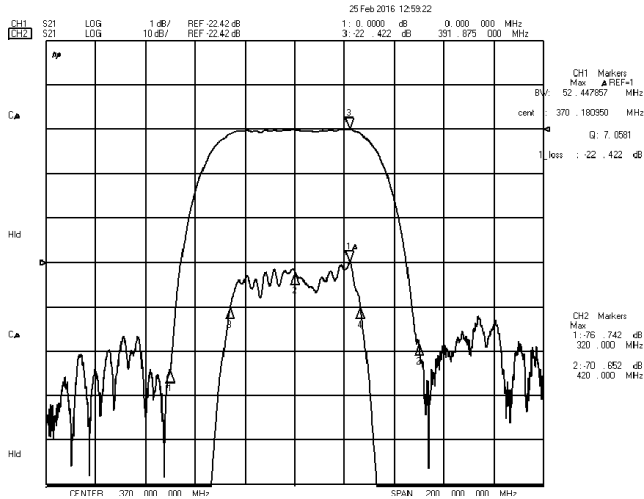
Terminating source impedance: 50Ω

Terminating load impedance: 50Ω

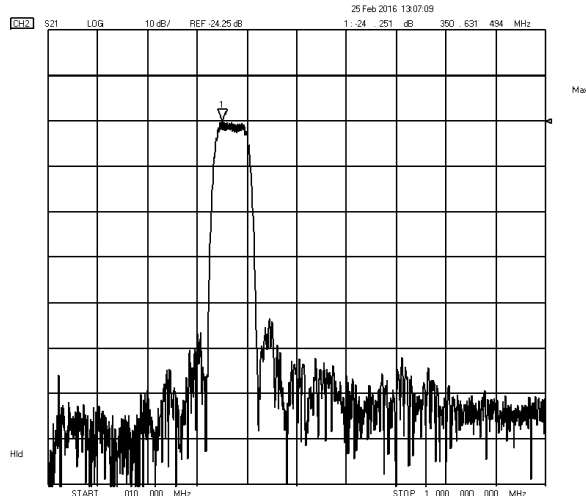
Item		Minimum	Typical	Maximum	Unit
Center Frequency	f_c		370.0		MHz
Insertion Loss(min)	IL		23.0	25.0	dB
Amplitude Ripple	$\Delta\alpha$		0.7	1.0	dB
1 dB Bandwidth	BW_{1dB}	40.0	52.4		MHz
Absolute Delay 355.00-385.00MHz	AD	7.8	7.9	8.0	us
Absolute Attenuation	α				
	320.00MHz	40.0	50.0		dB
	420.00MHz	40.0	47.0		dB

Frequency Characteristics

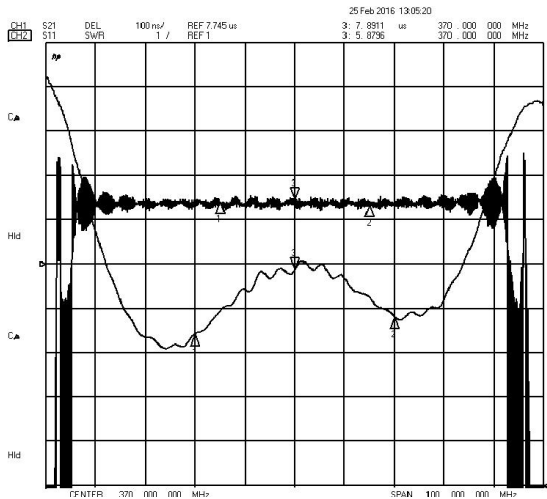
Frequency Response



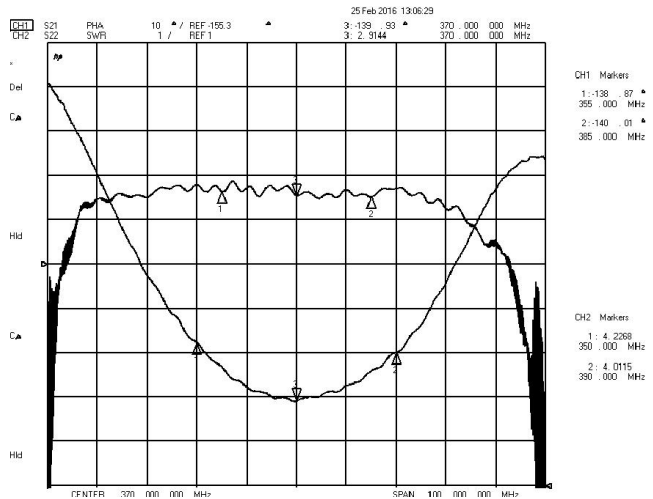
Frequency Response (wideband)



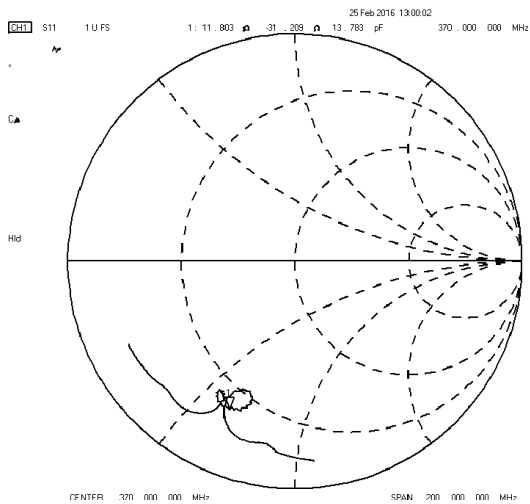
Delay Ripple & S11 VSWR



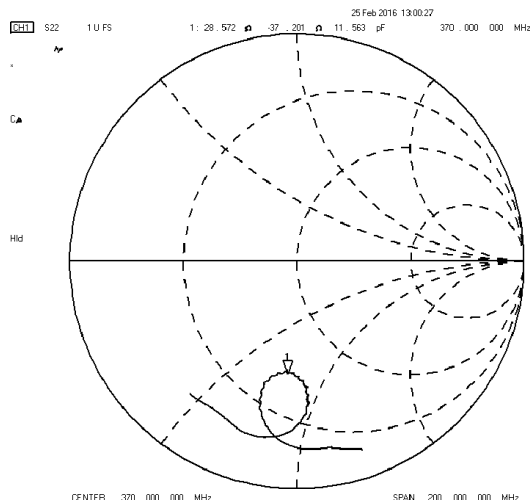
Phase Linearity & S22 VSWR



S11 Smith Chart



S22 Smith Chart



Notes

1. As a result of the particularity of inner structure of SAW products, it easy to be breakdown by electrostatic, so we should pay attention to **ESD protect** in the test.
2. **Static voltage** between signal load and ground may cause deterioration and destruction of the component. Please avoid static voltage.
3. **Ultrasonic cleaning** may cause deterioration and destruction of the component. Please avoid ultrasonic cleaning.
4. Only leads of component may **be soldered**. Please avoid soldering another part of component.
5. There is a close relationship between the device's performance and **matching network**. The specifications of this device are based on the test circuit shown above. L and C values may change depending on board layout. Values shown are intended as a guide only.