



APPROVAL SHEET

Approval Specification	Customer's Approval Certificate
TO:	Please return this copy as a certification of your approval
Part No.:	Checked & Approved by:
Customer's Part No.:	Date:

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Part No.	:	SF1596
Pages	:	6
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Revision	:	1.0

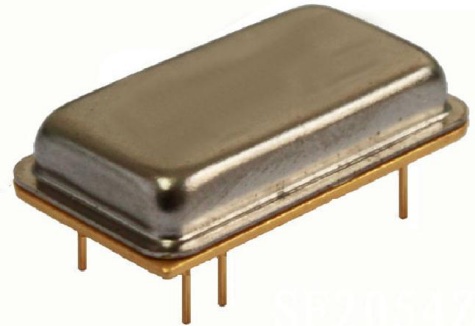
Prepared by:	刘菲
Checked by:	
Approved by:	

Application

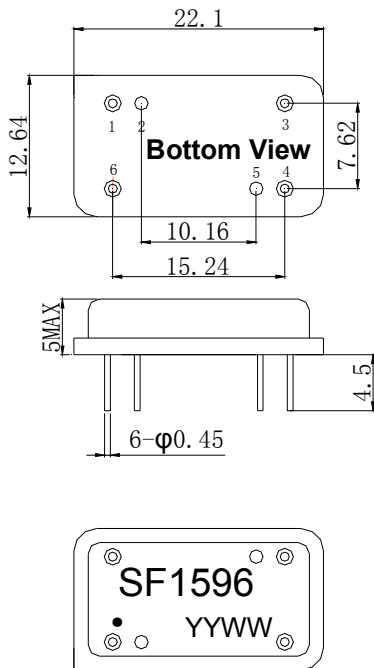
- High-loss SAW component
- Low amplitude ripple
- Sharp rejections at both out-bands
- Passband 12.4 MHz

Features

- RoHS compatible
- Package size 22.1x12.64x5.00mm³
- Package Code DIP2212
- Electrostatic Sensitive Device(ESD)



Package Dimensions (Unit: mm)



Pin Configuration

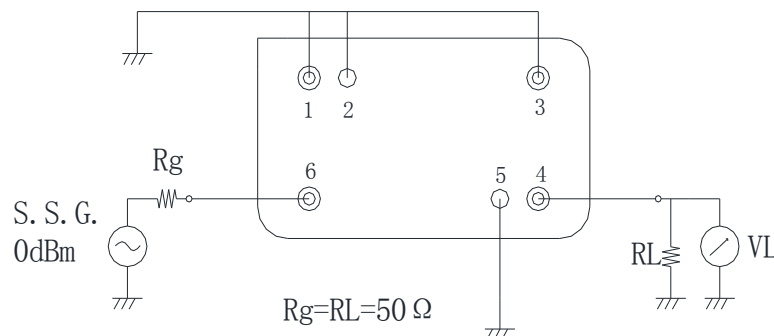
Pin No.	Description
6	Input
4	Output
1,2,3,5	Ground

Marking Description

S	Trademark
F	SAW Filter
1596	Part Number
●	Pin 1
YYWW	Year Code & Week Code

*Fig: If the products produced in 06th week of 2012, The year code & week code is 1206.

Test Circuit(Bottom View)



Performance**Maximum Rating**

Item		Value	Unit
DC Voltage	V _{DC}	3	V
Operation Temperature	T	-40 ~ +85	°C
Storage Temperature	T _{stg}	-55 ~ +125	°C
RF Power Dissipation	P	10	dBm

Electronic Characteristics

Test Temperature: 25°C ± 2°C

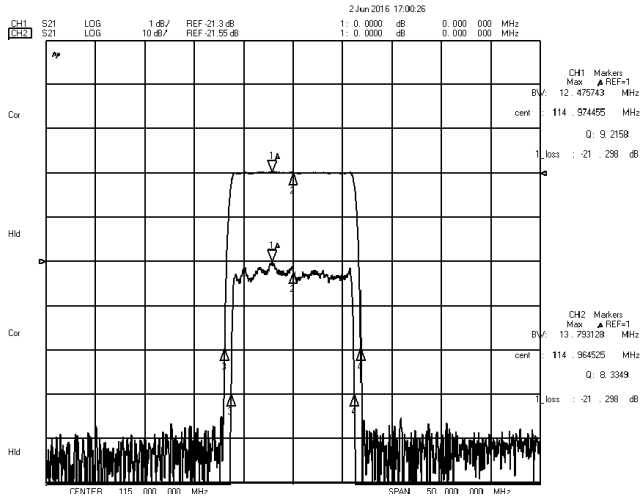
Terminating source impedance: 50Ω

Terminating load impedance: 50Ω

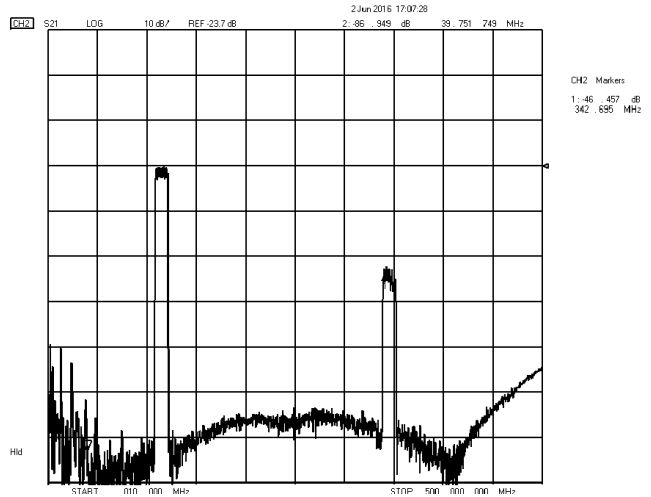
Item		Minimum	Typical	Maximum	Unit
Center Frequency	f _c	114.8	115.0	115.2	MHz
Insertion Loss(min)	IL		21.3	25.0	dB
Amplitude Ripple (p-p)	Δα		0.4	1.0	dB
3 dB Bandwidth	BW _{3dB}		12.4		MHz
10 dB Bandwidth	BW _{10dB}		13.0	13.1	MHz
40 dB Bandwidth	BW _{40dB}		13.8	14.4	MHz
Absolute Delay@Fc	AD		2.2		us
Phase Linearity			5.0	10.0	deg
	108.8-121.2MHz				
Absolute Attenuation	α				
	97.5MHz	55.0	58.0		dB
	132.5 MHz	55.0	58.0		dB

Frequency Characteristics

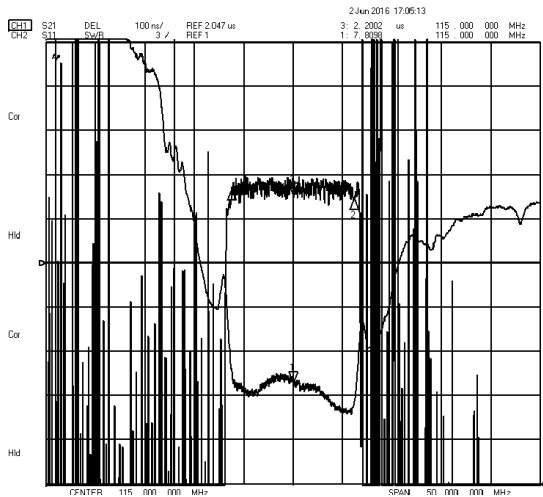
Frequency Response



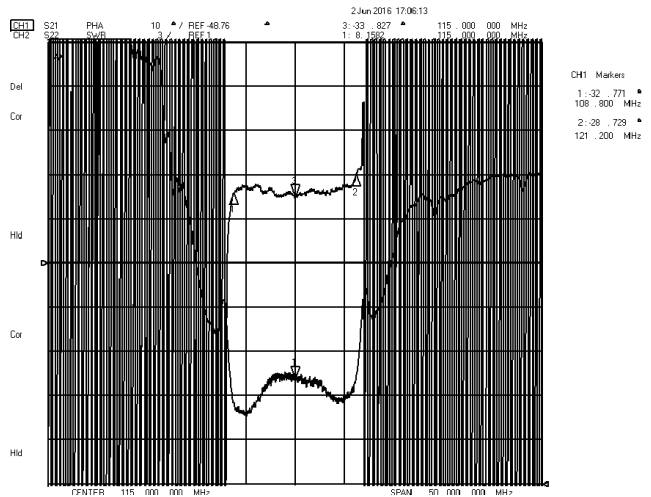
Frequency Response (wideband)



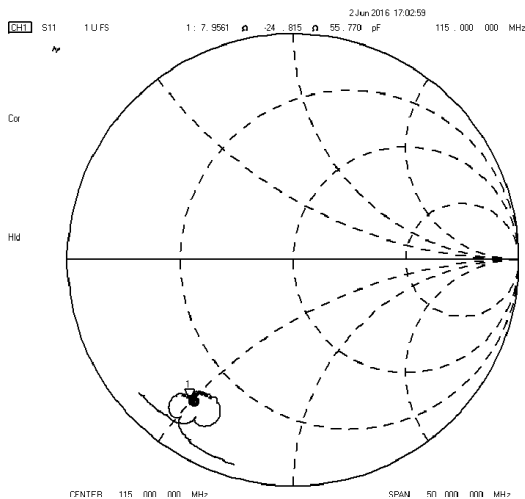
Delay Ripple & S11 VSWR



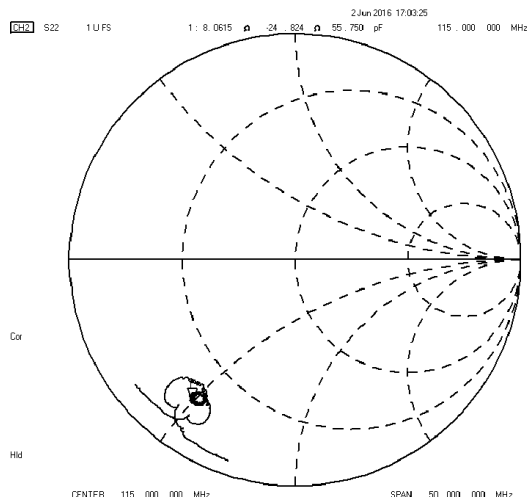
Phase Linearity & S22 VSWR



S11 Smith Chart



S22 Smith Chart



Notes

1. As a result of the particularity of inner structure of SAW products, it easy to be breakdown by electrostatic, so we should pay attention to **ESD protect** in the test.
2. **Static voltage** between signal load and ground may cause deterioration and destruction of the component. Please avoid static voltage.
3. **Ultrasonic cleaning** may cause deterioration and destruction of the component. Please avoid ultrasonic cleaning.
4. Only leads of component may **be soldered**. Please avoid soldering another part of component.
5. There is a close relationship between the device's performance and **matching network**. The specifications of this device are based on the test circuit shown above. L and C values may change depending on board layout. Values shown are intended as a guide only.